

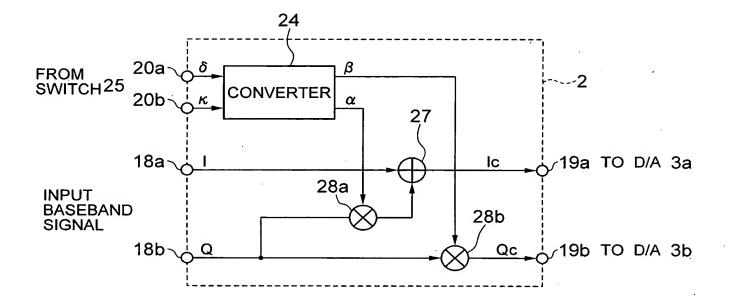
GPIB INTERFACE | RANSMITTER | TESTER | CONTROL TERMINAL (PC) SERIAL INTERFACE (RS-232C) တ က RADIO TRANSMITTING UNIT 。 06 32b 32c Q **PROCESSOR** <u>=</u> 8 힏 6a eb 9 <u>5</u>b 17a **5**a D/A - LPF Qtx **4**a 6 Ä MEMORY <del>4</del> <del>6</del> D/A 9 VECTOR CORRECTOR 25 6 Ø 26a <u>~</u> 26b BASEBAND SIGNAL GENERATOR 9

ATT

g

FIG. 1B

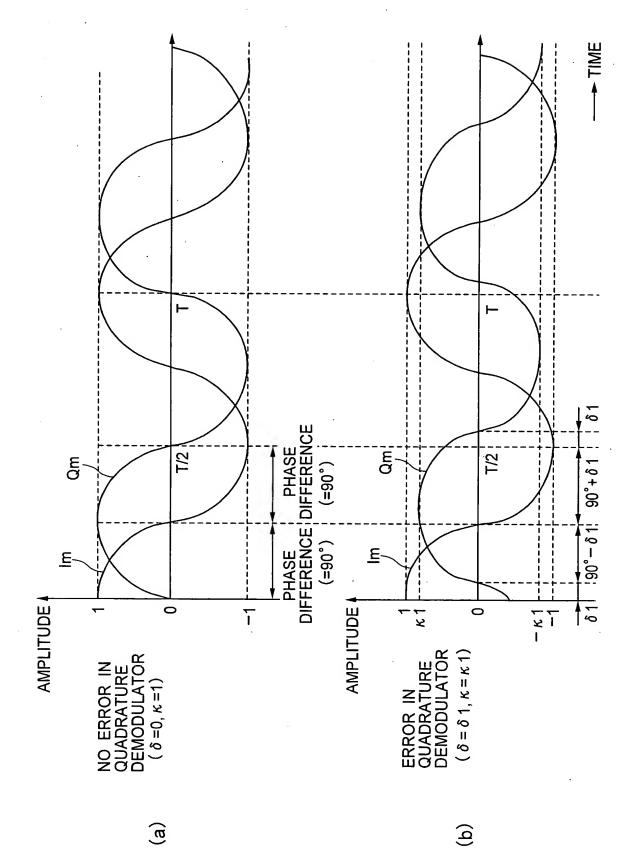
FIG. 2

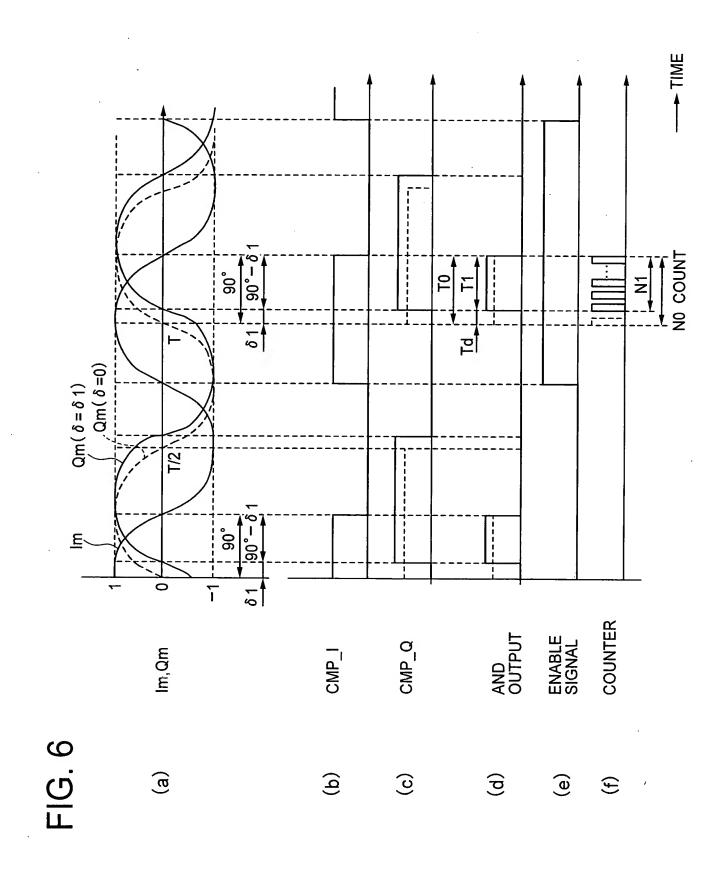


*FERMINATOR* 3 9 For For Log 190° 30 LO! Qm g <u>=</u> 헏 6a AMPLITUDE DIFFERENCE DETECTOR DIFFERENCE 9 DETECTOR **2**b **5a** 4 -D/A Otx -61 .40 z 15 CONTROL MEMORY D/A D/A 16 38, 25 SWITCH ጻ Ø 26a 18 <del>1</del>9 <u>a</u> 26b BASEBAND SIGNAL GENERATOR 100

FIG. 3







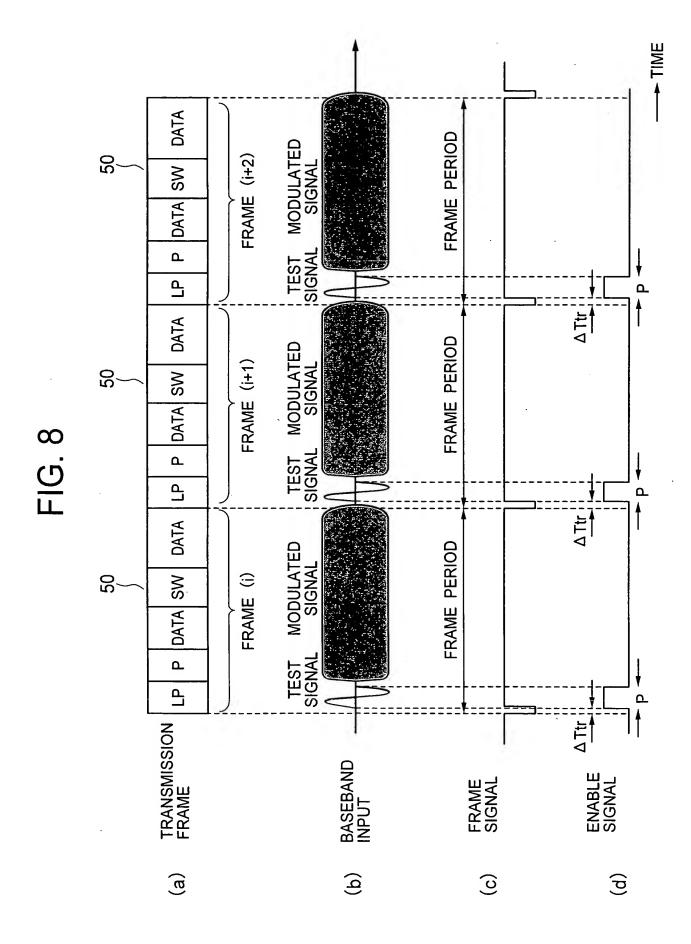


FIG. 9

FIG. 10

